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|  | SHRI SOMESHWAR SHIKSHAN PRASARAK MANDAL’S  **SHARADCHANDRA PAWAR COLLEGE OF ENGINEERING AND TECHNOLOGY** | Record No:- |
| Revision:- |
| Date:-02/08/2021 |



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| **PRACTICAL PLAN** |

**Department:** Computer Engineering **Academic Year:** 2021-22

**Semester:** I **Class:** T.E **Subject:** *Laboratory Practice- I*

**Date:** 02/08/2021

**Teaching Scheme: Lectures/Week:** 00 **Practical/Week:** 02 **Tutorials/Week:** 00

**Examination Scheme: Insem: Online:** NA **Endsem:**

| **Exp.**  **No** | **Name of Experiment** | **Batch** | **Planned Date** | **Conducted Date** | **Sign of Faculty** |
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| 1 | Design suitable Data structures and implement Pass-I and Pass-II of a two-pass assembler for pseudo-machine. Implementation should consist of a few instructions from each category and few assembler directives. The output of Pass-I (intermediate code file and symbol table) should be input for Pass-II |  |  |  |  |
| 2 | Design suitable data structures and implement Pass-I and Pass-II of a two-pass macroprocessor. The output of Pass-I (MNT, MDT and intermediate code file without any macro definitions) should be input for Pass-II. |  |  |  |  |
| 3 | Write a program to simulate CPU Scheduling Algorithms: FCFS, SJF (Preemptive), Priority  (Non-Preemptive) and Round Robin (Preemptive). |  |  |  |  |
| 4 | Write a program to simulate Page replacement algorithm. |  |  |  |  |

**Subject In charge Head of the Department Principal**